

In the Drawings:

Please replace Figure 3 with the enclosed new Figure 3 showing the uplink processor 39A and the downlink processor 39B of the MLC 39 as described at page 9, line 9 and page 9, line 26.



BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP (303) 740-1980
Title: "Dual Mode Uplink/Downlink Location Measurement and Multi-
Protocol Location"
Inventors: Davis, G.
Filing Date: 10/27/00

Attorney Docket No: 42P11619

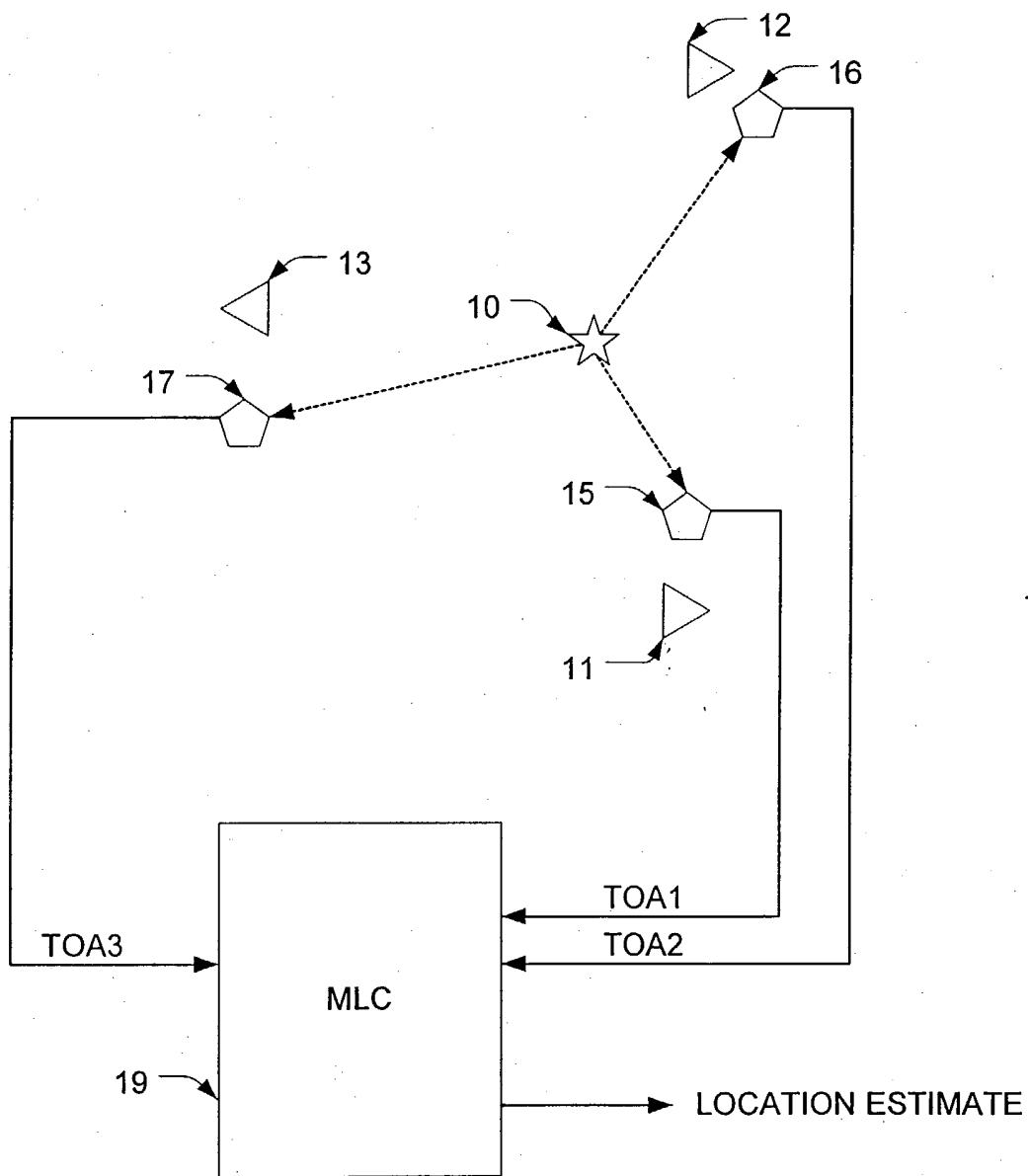


FIG. 1
PRIOR ART



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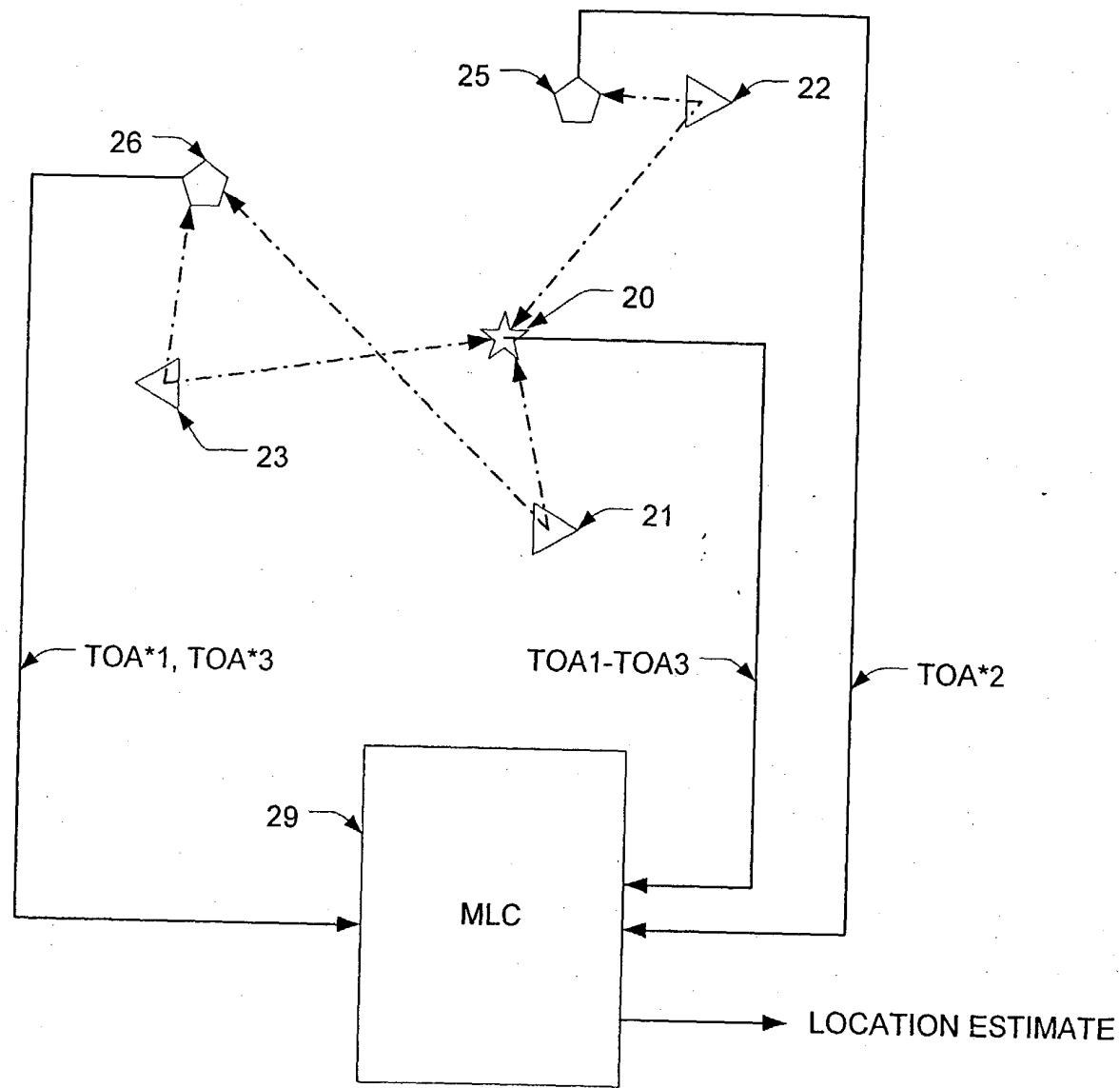


FIG. 2
PRIOR ART



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REPLACEMENT SHEET

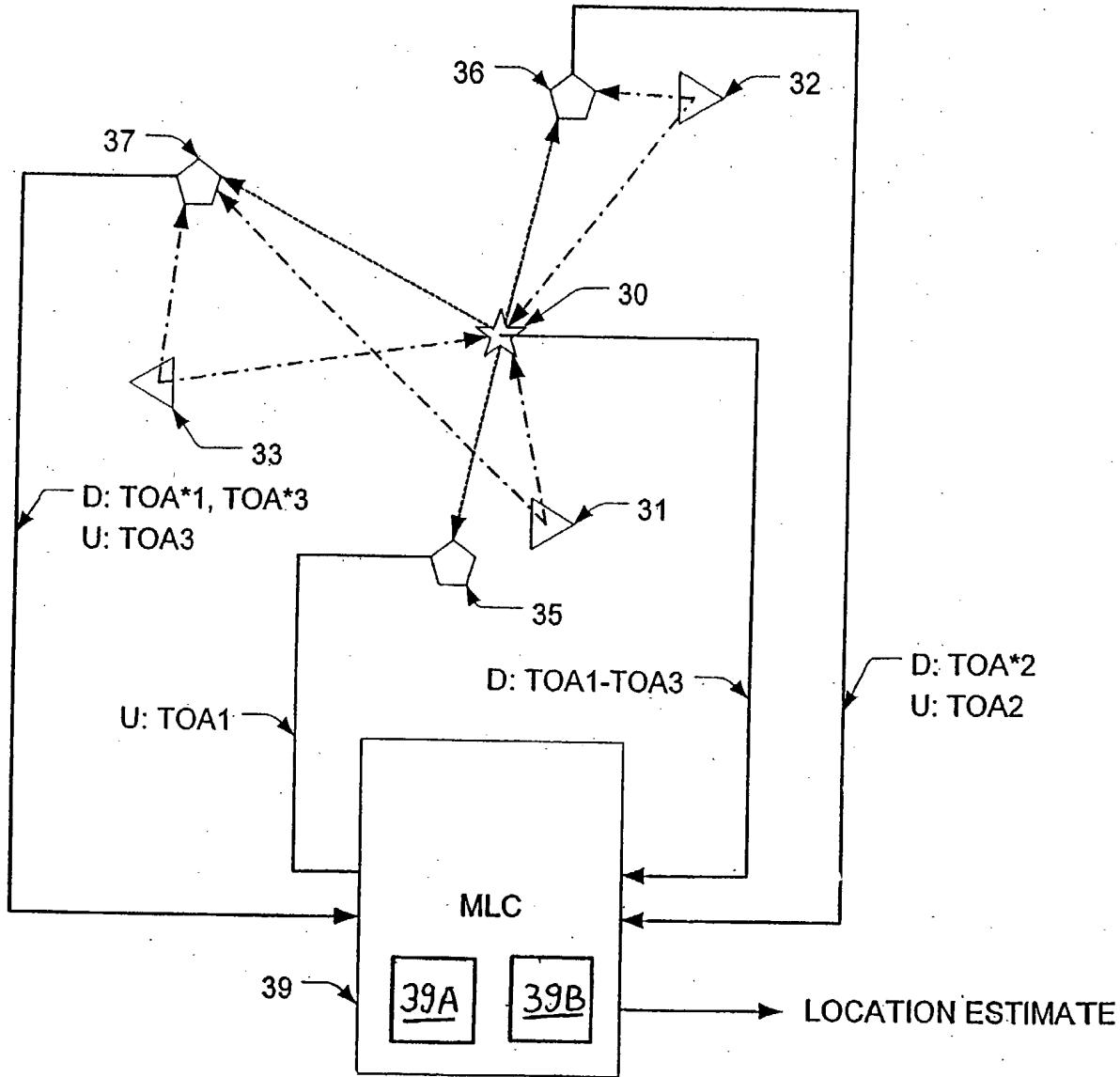


FIG. 3



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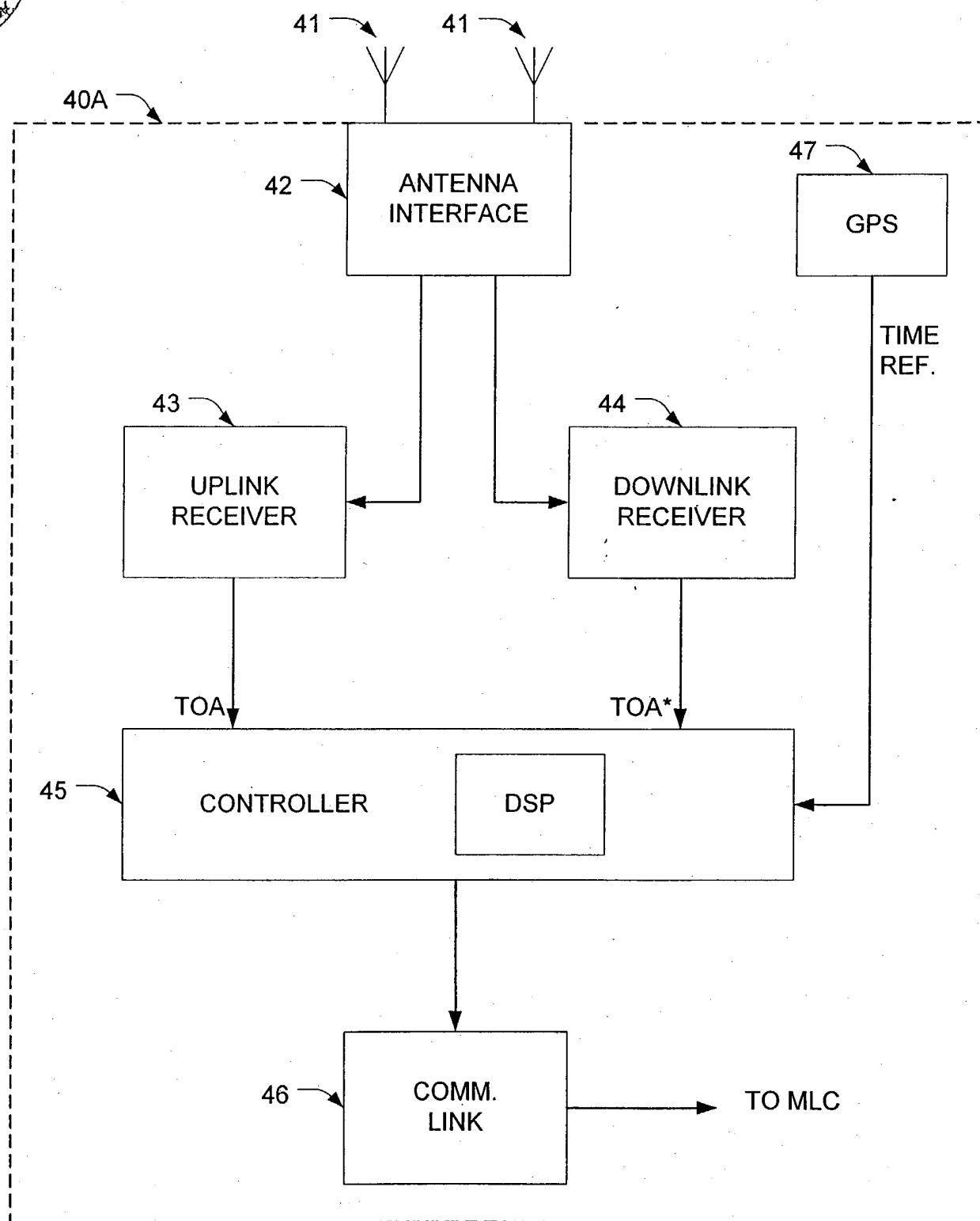


FIG. 4A

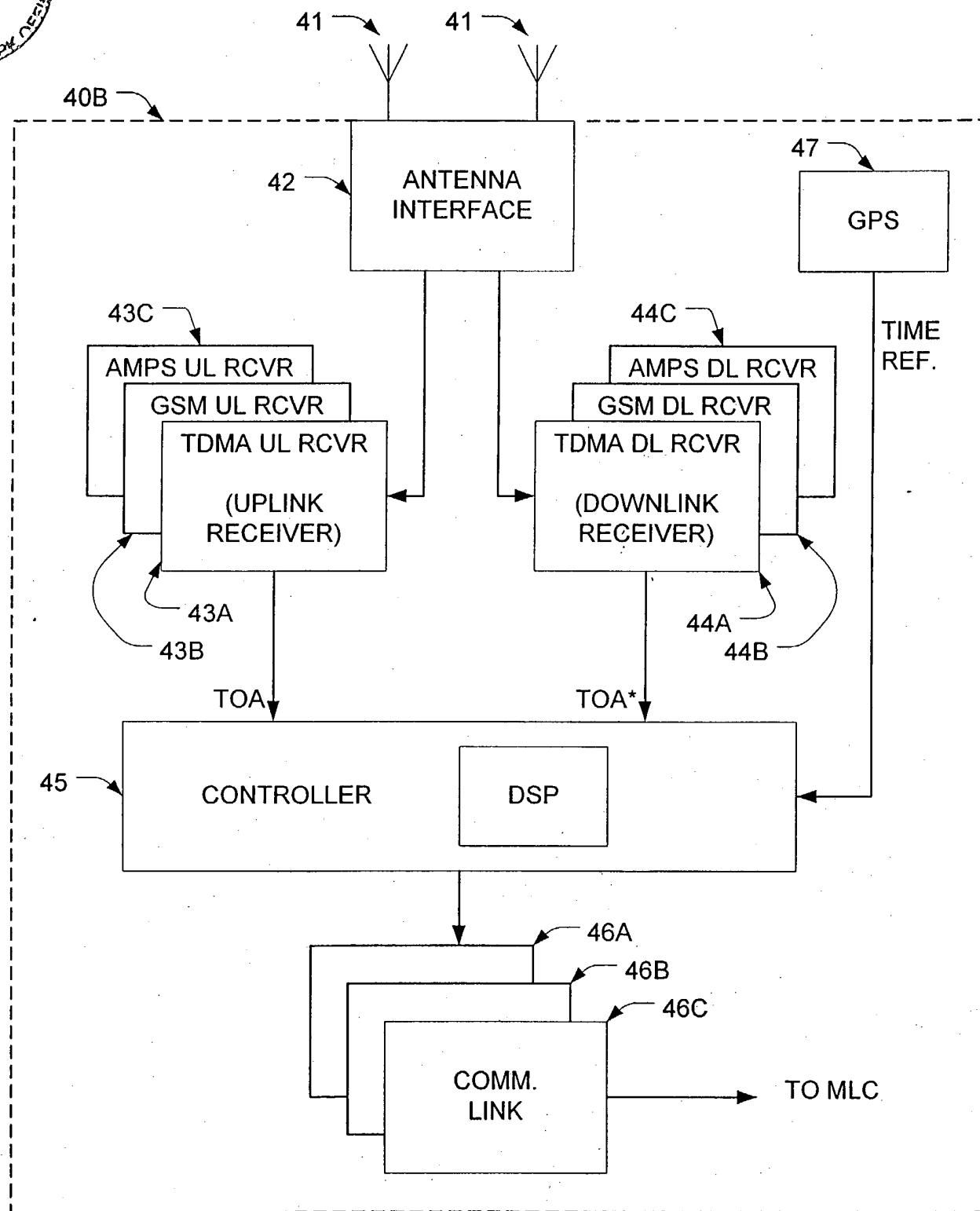


FIG. 4B

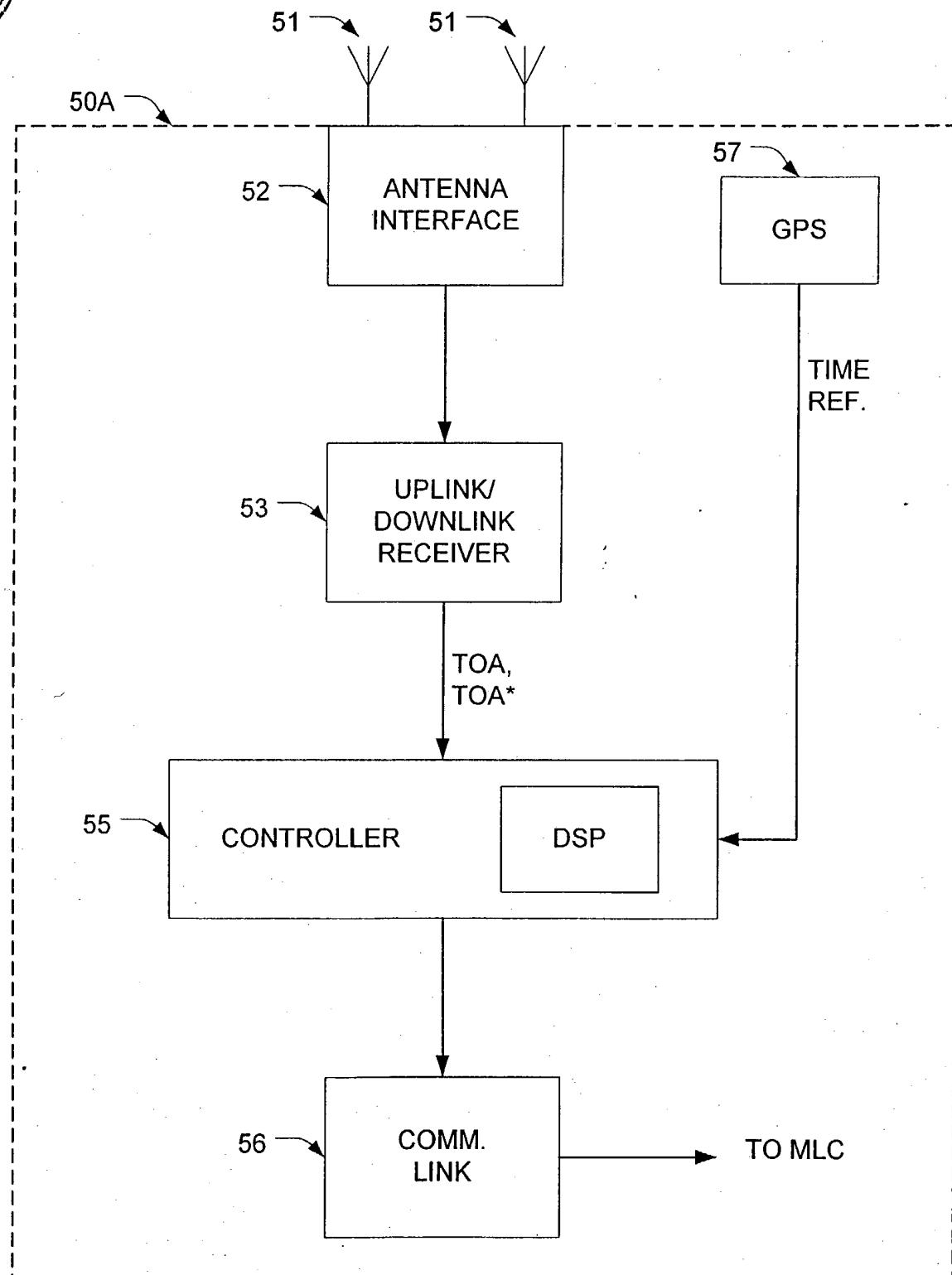


FIG. 5A

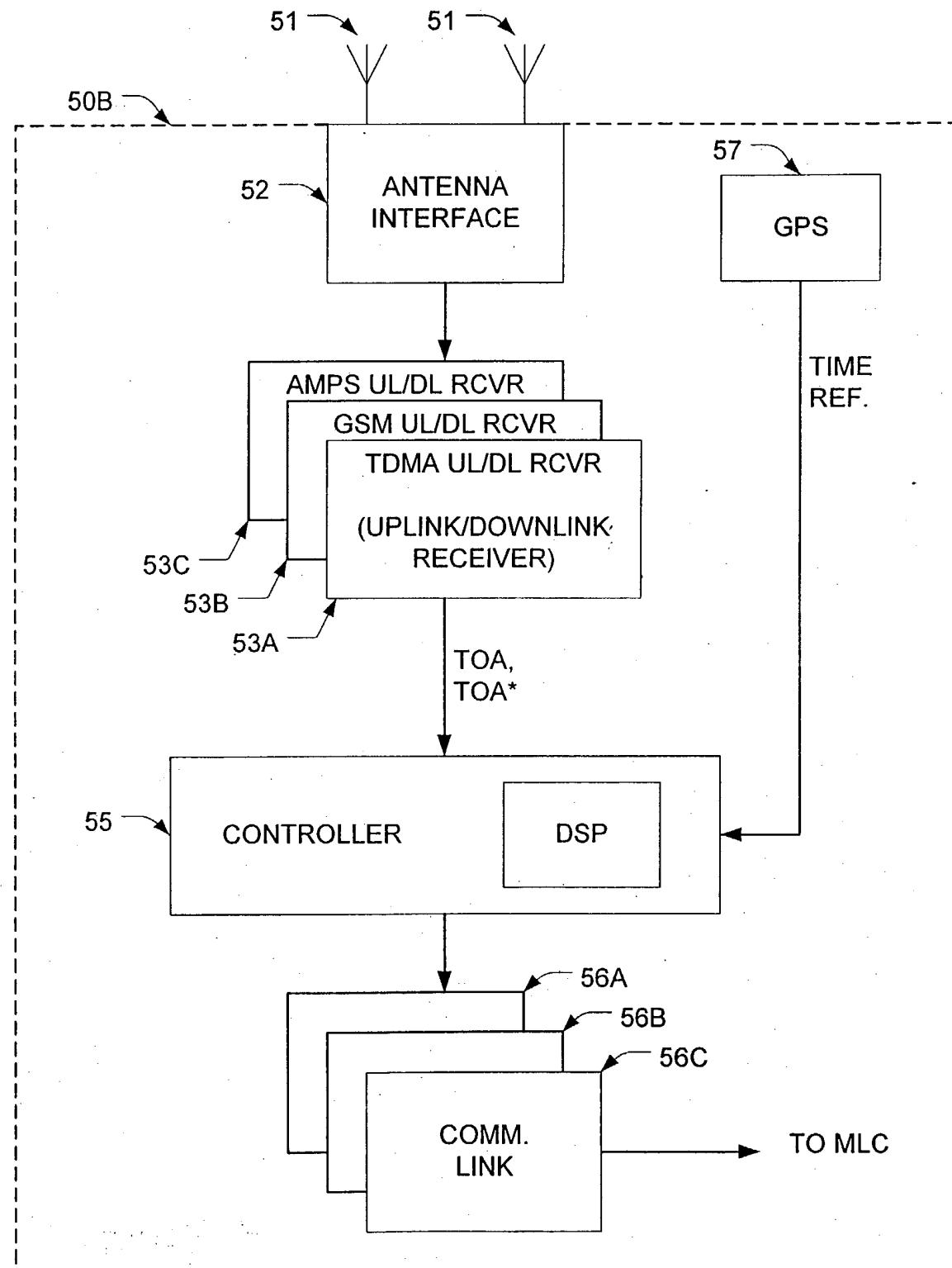


FIG. 5B



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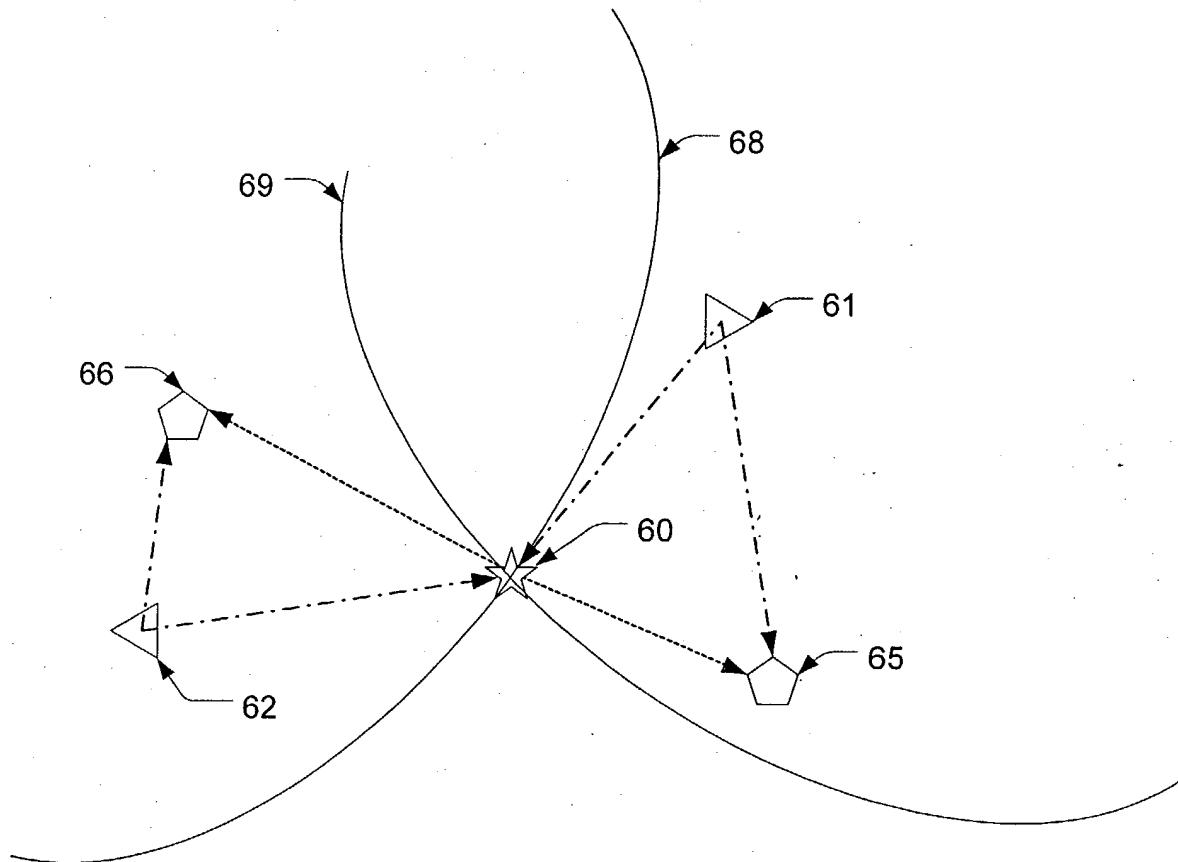


FIG. 6